

# Channel coding Convolutional codes

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#### Index

- Codes with memory
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#### Linear block codes v. convolutional codes

A few (related) differences...

Codes with memory

Convolutional codes

Linear block codes

A few (related) differences...

Convolutional codes	Linear block codes
• Encoding is <i>continuous</i>	• Encoding is <i>blockwise</i>

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Convolutional codes	Linear block codes
<ul><li> Encoding is <i>continuous</i></li><li> System has <b>memory</b></li></ul>	<ul><li>Encoding is blockwise</li><li>System has no memory</li></ul>

#### Linear block codes v. convolutional codes

#### A few (related) differences...

Convolutional codes	Linear block codes	
<ul><li> Encoding is continuous</li><li> System has memory</li><li> Sequence-to-sequence mapping</li></ul>	<ul> <li>Encoding is blockwise</li> <li>System has no memory</li> <li>Message-to-codeword mapping</li> </ul>	

#### A few (related) differences...

Codes with memory

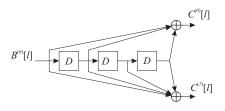
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In both schemes, every operation (e.g., convolution) is in GF(2).

# Specification

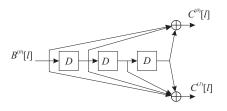
Codes with memory

Codes are often specified through a block diagram that illustrates how the input is transformed into the output, e.g.



- $B^{(j)}[I]$  is the I-th bit of the *j*-th input
- $C^{(j)}[I]$  is the I-th bit of the *j*-th output
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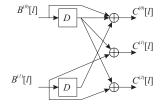


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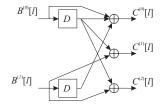


# The system has memory

The output bits depend on previous (and current) inputs: this is a state machine



• Connection between the inputs and the outputs

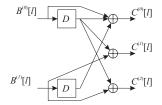


• Connection between the inputs and the outputs

$$C^{(0)}[I] = B^{(0)}[I] + B^{(0)}[I-1] + B^{(1)}[I-1]$$

Codes with memory

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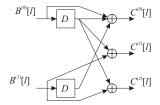
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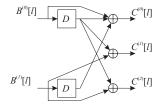
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Codes with memory

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• we express this relations in the D domain...

### Convolution

...but, where is the convolution in a convolutional code?

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# (A) Convolution of discrete-time signals

$$x[n] * h[n] = h[n] * x[n] = \sum_{k=-\infty}^{\infty} h[k]x[n-k]$$

and assuming the impulse response, h[k], is non-zero between time instants, e.g., 0 and 1

$$= h[0]x[n] + h[1]x[n-1]$$

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For the first output, e.g., we have

$$C^{(0)}[I] = \underbrace{B^{(0)}[I] + B^{(0)}[I-1]}_{B^{(0)}[I] * [1-1]} + \underbrace{B^{(1)}[I-1]}_{B^{(1)}[I] * [0-1]}$$

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Every output is a sum of convolutions!!

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#### **Definition:** The *D* transform...

...of a binary sequence  $B^{(i)}[I]$  is

$$B^{(i)}(D) = \sum_{u} B^{(i)}[u] \cdot D^{u}$$
$$= \cdots B^{(i)}[-1] \cdot D^{-1} + B^{(i)}[0] + B^{(i)}[1] \cdot D^{1} + \cdots$$

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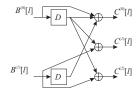
with the property

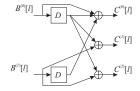
$$B^{(i)}[I-d] \leftrightarrow B^{(i)}(D) \cdot D^d$$

so that, e.g.,

$$B^{(i)}[I] + B^{(i)}[I-1] + B^{(i)}[I-2] + B^{(i)}[I-3] \stackrel{D}{\longleftrightarrow} B^{(i)}(D)(1+D+D^2+D^3).$$

### Generator matrix





In polynomial form

$$C^{(0)}(D) = (1+D)B^{(0)}(D) + DB^{(1)}(D)$$

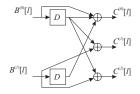
$$C^{(1)}(D) = DB^{(0)}(D) + B^{(1)}(D)$$

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#### Generator matrix

Codes with memory

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• Using matrices: C(D) = B(D)G(D), with

$$\mathbf{C}(D) = \begin{bmatrix} C^{(0)}(D) & C^{(1)}(D) & C^{(2)}(D) \end{bmatrix} \quad \mathbf{B}(D) = \begin{bmatrix} B^{(0)}(D) & B^{(1)}(D) \end{bmatrix}$$

$$\mathbf{G}(D) = \begin{bmatrix} 1+D & D & D \\ D & 1 & 1 \end{bmatrix}_{k \times n} \qquad \begin{matrix} \mathbf{G} \equiv \text{generator matrix} \\ \mathbf{g}_{ij} \equiv \text{contribution of the } i\text{-th input} \\ \text{to the j-th output} \end{matrix}$$

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#### **Definition:** Overall memory...

...of the code,  $M_t$ , is the numer of delay units in the coding scheme.

$$M_t = \sum_{i=0}^{k-1} M^{(i)}$$

with

$$M^{(i)} = \max_{i} \operatorname{degree}(g_{ij}(D)) \equiv \operatorname{memory} i$$
-th input

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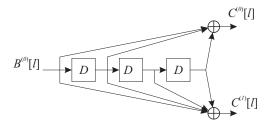
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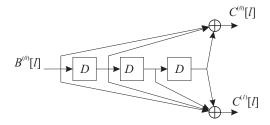
$$K = 1 + \max_{i,j} \mathsf{degree} \ (g_{ij}(D))$$

A convolutional code can also be **systematic** (same definition).

### The encoder as a finite-state machine



#### The encoder as a finite-state machine

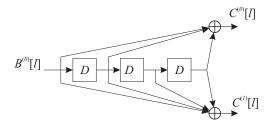


 The state of the encoder is given by the bits stored (yielded as output) in the delay elements, here

$$\Psi \equiv (B^{(0)}[l-1], B^{(0)}[l-2], B^{(0)}[l-3]).$$

#### The encoder as a finite-state machine

Codes with memory



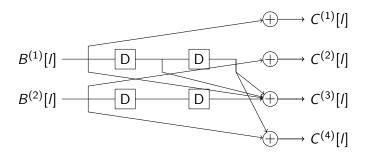
• The *state* of the encoder is given by the bits stored (yielded as output) in the delay elements, here

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• In general there are  $2^{M_t}$  possible states (bits from delay elements across all the inputs are stacked together). A possible mapping here:

$$egin{aligned} \Psi_0 & 
ightarrow (0,0,0) & \Psi_1 
ightarrow (1,0,0) & \Psi_2 
ightarrow (0,1,0) & \Psi_3 
ightarrow (1,1,0) \ \Psi_4 
ightarrow (0,0,1) & \Psi_5 
ightarrow (1,0,1) & \Psi_6 
ightarrow (0,1,1) & \Psi_7 
ightarrow (1,1,1) \end{aligned}$$

Codes with memory



Let us take a look at the evolution of the state of the system for a simple example...

	-2	-1	0	+1
$B^{(1)}[I]$	0	0	1	1
$B^{(2)}[I]$	0	0	0	1
	previous bits		bits to be encoded	

Initial state:

$$\Psi=[{\color{red}0,0,0,0,1}]$$

#### Initial state:

$$\Psi = [0, 0, 0, 0, ]$$

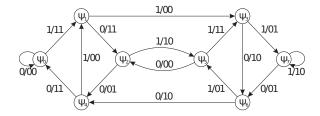
$$\Psi = [0, 0, 0, 0, 0]$$

$$B^{(1)}[0] = 1$$

$$B^{(2)}[0] = 0$$

$$B^{(1)}[0] \qquad \boxed{D} \qquad \boxed{D} \qquad \boxed{D} \qquad \boxed{Initial state:} \\ B^{(2)}[0] \qquad \boxed{D} \qquad \boxed{D} \qquad \boxed{0} \qquad \boxed{\Psi = [0,0,0,0,]} \\ 1 \qquad \boxed{D} \qquad$$

# State diagram



#### Every arrow is labeled with

- the input bits triggering that transition, and
- the output bits originating from that state given the corresponding input bits.

### Index

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- Opening
- 4 Turbo codes

# .....0

Straightforward once we know

- the initial state
- the state diagram



Let us assume we start from state  $\Psi_0$  and we want to encode the sequence [001]. The result is

[00 00 11]

Straightforward once we know

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### **Example**

Let us assume we start from state  $\Psi_0$  and we want to encode the sequence [001]. The result is

 $[00\ 00\ 11]$ 



#### → Header

After the information sequence, a header is transmitted to force the encoder to go back to its initial state.

information

header

### Index

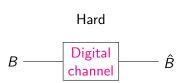
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# Decoding



• Metric: **Hamming** distance

### Decoding

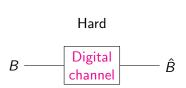


• Metric: Hamming distance



 Metric: Euclidean distance (at every step, squared difference)

# Decoding



• Metric: **Hamming** distance



 Metric: Euclidean distance (at every step, squared difference)

#### In both cases:

- the goal is to find the *full* sequence most likely transmitted
- the solution is given by the Viterbi algorithm

### Some keys

• We need to assess all the trajectories starting a the initial state (usually  $\Psi_0$ ).

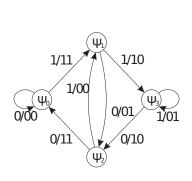
- We need to assess all the trajectories starting a the initial state (usually  $\Psi_0$ ).
- For every possible transition, we compare its corresponding output with the observed one.

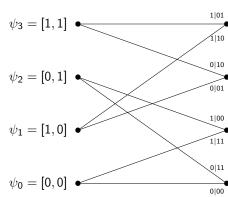
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- At every time instant, for every possible state, we need to find the path reaching it with the smallest accumulated cost.

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- Whenever two paths reach the same state, we keep the one with the smallest *accumulated* cost.

- We need to assess all the trajectories starting a the initial state (usually  $\Psi_0$ ).
- For every possible transition, we compare its corresponding output with the observed one.
- At every time instant, for every possible state, we need to find the path reaching it with the smallest accumulated cost.
- Whenever two paths reach the same state, we keep the one with the smallest accumulated cost.
- Decoding must end up in the initial state since a *header* is appended to every transmitted sequence in order to enforce this.

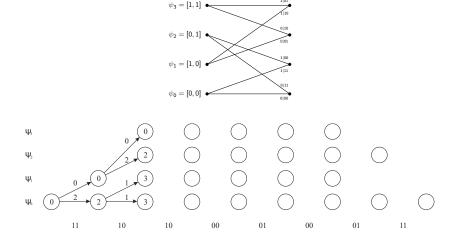
- Input sequence: 1 1 0 1 0 1 0 0
- Received sequence: 11 10 10 00 01 00 01 11



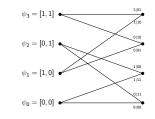


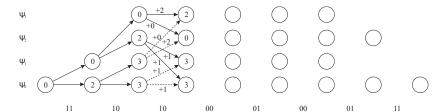
( **Trellis** representation )

#### Two first iterations

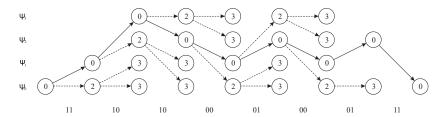


#### Third iteration



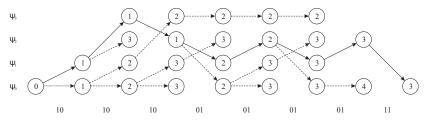


#### Final result



# Example with errors

- Received sequence: 10 10 10 01 01 01 01 11
- Final result



• The states sequence  $\Psi_0\Psi_1\Psi_3\Psi_2\Psi_1\Psi_2\Psi_1\Psi_2\Psi_0$  is associated with the input sequence 11010100

#### Performance

Soft decoding

$$P_{\rm e} pprox \kappa_2 {
m Q} \left( \sqrt{rac{2 D_{min} E_s}{N_0}} 
ight)$$

where  $\kappa_2$  is the number of bit errors (in the decoded sequence) caused by the sequence associated with  $D_{min}$ .

#### Performance

Codes with memory

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Hard decoding

$$P_{e} \approx \kappa_{2} \sum_{i=\lfloor (D_{min}-1)/2 \rfloor+1}^{nz} {nz \choose i} \epsilon^{i} (1-\epsilon)^{nz-i}$$

where z is the length of the trajectory associated with  $D_{min}$ and  $\epsilon$  is the bit error probability.

# Finding the minimum distance $D_{min}$

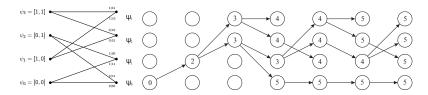
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#### Goal

We seek the sequence of states (path) that starts at the all-zeros sequence and goes back to it with the smallest accumulated cost.

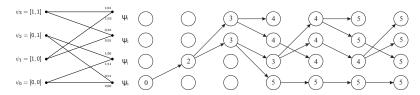


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#### Goal

Codes with memory

We seek the sequence of states (path) that starts at the all-zeros sequence and goes back to it with the smallest accumulated cost.



 $D_{min}$  allows computing the remaining parameters that have an impact on the performance

$$D_{min} = 5 \Rightarrow \begin{cases} \kappa_2 = 1 \\ z = 3 \end{cases}$$

# Soft decoding

For the sake of simplicity, let us assume antipodal modulation i.e.,  $A[I] = \pm A$ .

#### **Notation**

$$B_{0,:} = \{B[0], B[1], B[2], \dots\} \equiv \text{input bits}$$
  
 $q_{0,:} = \{q[0], q[1], q[2], \dots\} \equiv \text{soft estimates}$ 

2 possibilities

Codes with memory

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#### 2 possibilities

• **Sequence** soft decoding: it minimizes the sequence error probability

$$\hat{B}_{0,:} = \arg\max_{B_{0,:}} p(q_{0,:}|B_{0,:})$$

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• **Sequence** soft decoding: it minimizes the sequence error probability

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Bitwise soft decoding: it minimizes the bit error probability

$$\hat{B}[i] = \arg\max_{B[i]} p(B[i]|q_{0,:}), i = 0, 1, \cdots$$

#### $ML \text{ rule} \equiv MAP \text{ rule}$

$$\begin{split} \hat{B}_{0,:} &= \arg\max_{B_{0,:}} p(q_{0,:}|B_{0,:}) = \arg\max_{B_{0,:}} \prod_{I} p(q[I]|B[I]) \\ &= \arg\min_{B_{0,:}} \sum_{I} -\log p(q[I]|B[I]) \end{split}$$

where

Codes with memory

$$p(q[I]|B[I]) = \frac{1}{\sqrt{2\pi\sigma^2}} \exp\left(-\frac{(q[I] - A[I])^2}{2\sigma^2}\right).$$

Implemented by means of the Viterbi algorithm

# Bitwise soft decoding

#### MAP rule

We apply the maximum a posteriori (MAP) rule at the bit level to compute

$$P(B[I]=1|q_{0,:})$$

and we decide

$$\hat{B}[I] = \begin{cases} 1 & \text{if } p(B[I] = 1 | q_{0,:}) > P(B[I] = 0 | q_{0,:}) \\ 0 & \text{otherwise} \end{cases}$$

Implemented by means of **BCJR** (Bahl, Cocke, Jelinek and Raviv) algorithm

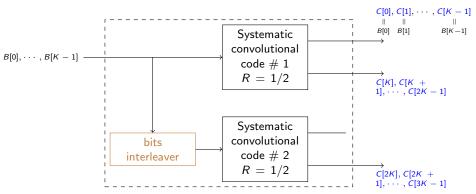
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### Turbo codes

 They are built by composing two convolutional codes that operate over bits ordered differently. Codes with memory

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- Main elements:
  - 2 convolutional codes
  - bit interleaver



### Remarks

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- Good performance in the low-SNR region (at about 0.7 dBs from Shannon limit).
- Regarding decoding...
  - It is iterative since the decoding of the original sequence and the *shuffled* one must agree.
  - Relies on BCJR algorithm.